


Timetable for B.Tech 5th Semester HONORS & MINORS Examinations, DECEMBER 2024

Admitted Batch - 2022			Academic Year: 2024-2025	
TIME:	02.00 PM TO 05.00 PM			
Date & Day	HONORS		MINORS	
	BRANCH	NAME OF THE SUBJECT	BRANCH	NAME OF THE SUBJECT
16.12.2024 (MON)	ECE	21ECH12 – CMOS Logic Circuit Design	CIVIL, EEE, MECH, ECE	21DSM02 – Principles of Artificial Intelligence
	CSE, IT & CSE- AI&ML	21CSH22 – Design Patterns		

NOTE: Student should report to the Examination Hall 30 Minutes before the commencement of the Examination.



ACE



Controller of Examinations